Function.

This board allows up to eight PROMs (Type 5204) to be connected onto the system bus. Decoding is provided for use with either a full 16 bit address bus or a 12 bit bus with page select.

Operation.

IC10 provides decoding so that each PROM is selected in turn. The edge connection to pin 1 provides for page selection (active-low). Alternatively IC9 can be used to provide an on-board page decoder from a full 16 bit address bus. The page is selected by linking the appropriate decoded lines to the succeeding decoder.

NOTE.

If all the system memory is located in page Ø, then IC9 can be omitted and L1 connected to ground.

Locations.

The PROMs are numbered in their address sequence and the following table shows the starting address of each:-

IC number	1	2	3	4	5	6	7	8
Start address	ØØØ	2ØØ	4ØØ	6ØØ	8ØØ	AØØ	CØØ	EØØ

e.g. PROM IC4 occupies the address space from 6ØØ to 7FF.

Links L1 and L2 select the 4k page address as follows:

Address	L1 to
ØØØØ	A
1ØØØ	B
2ØØØ	C
3ØØØ	D

Address	L2 to
ØØØØ	E
4ØØØ	F
8ØØØ	G
CØØØ	H

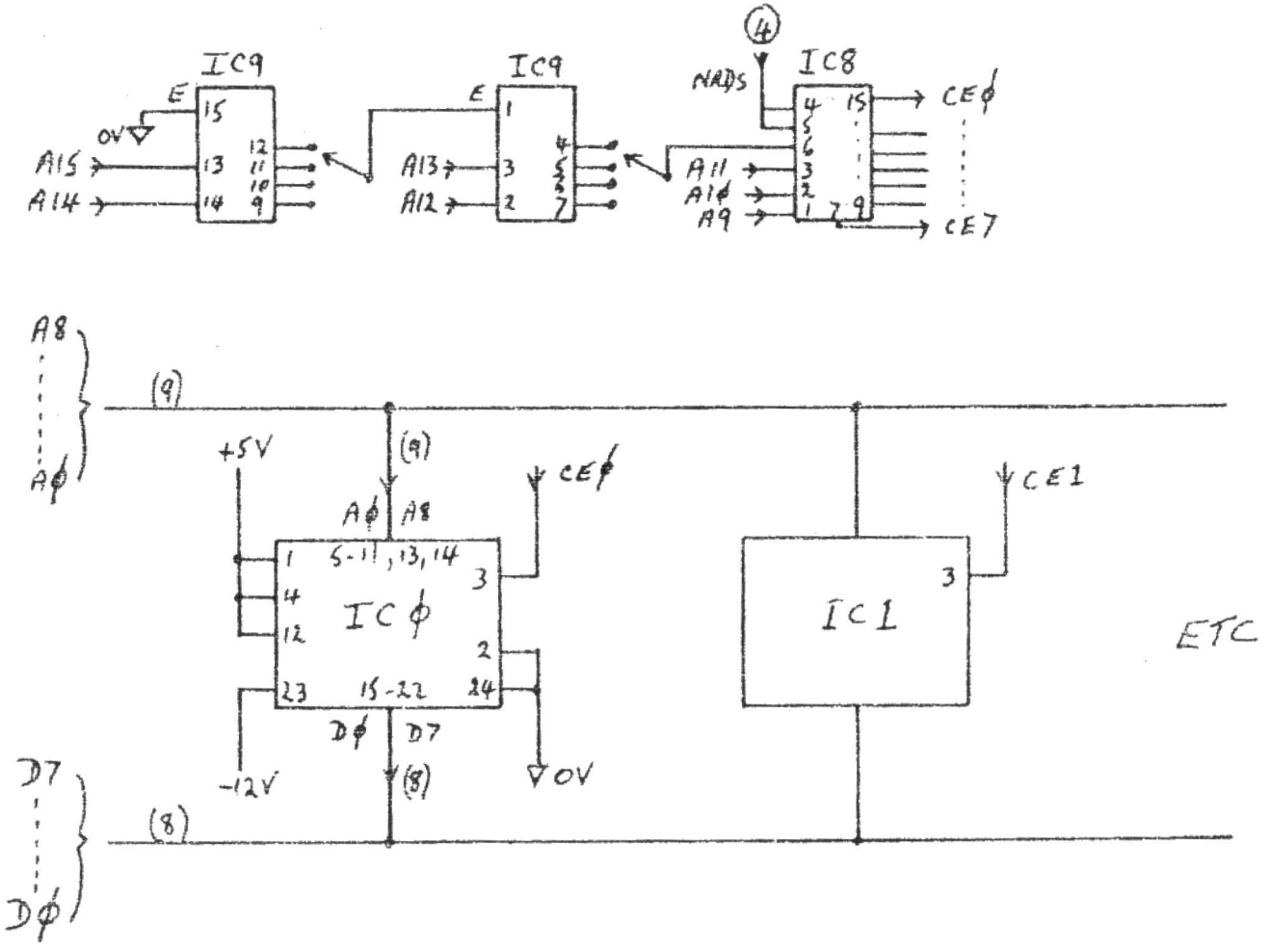
The two partial address values should be added together, i.e. L1 to C and L2 to F selects the page starting at 6ØØØ (2ØØØ + 4ØØØ).

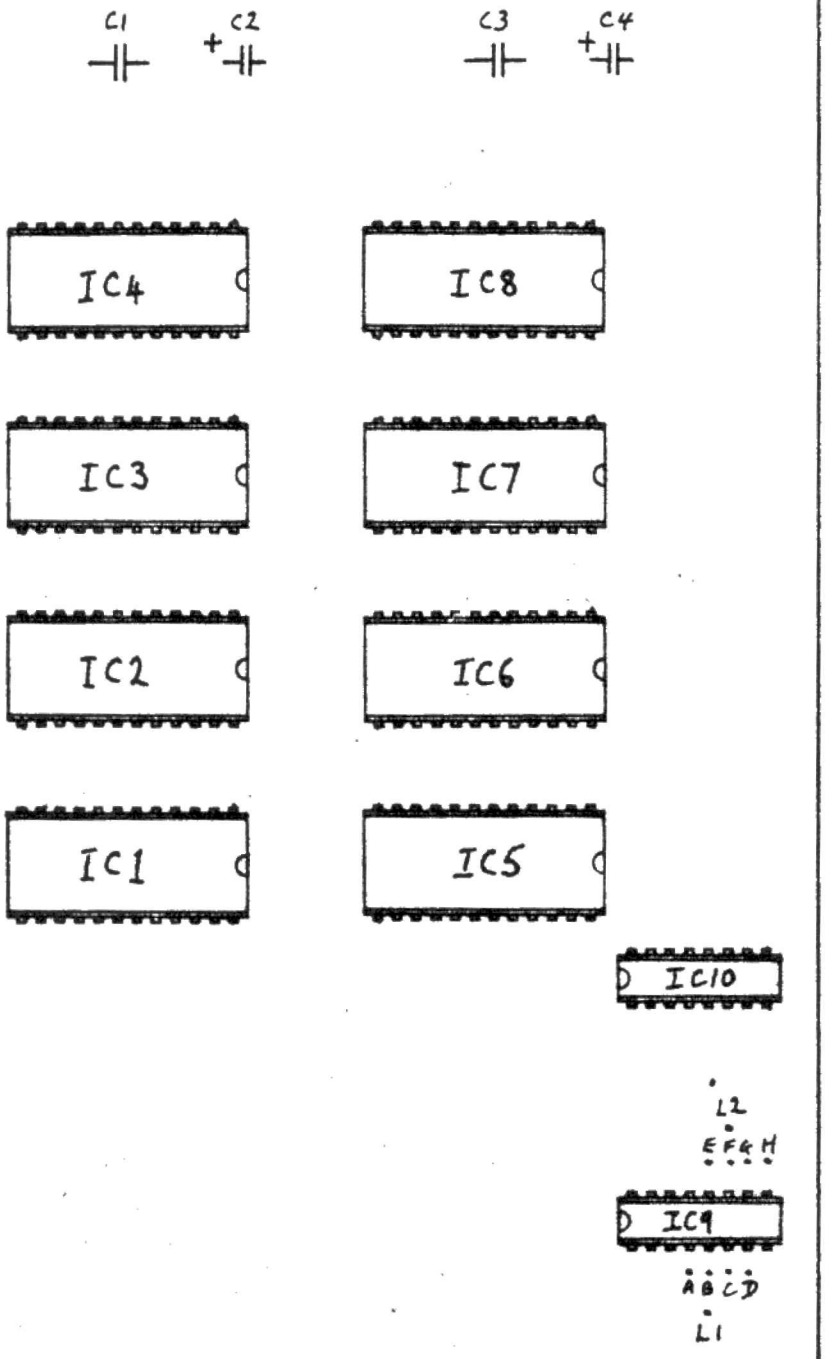
Drn. C.S.

Date 14/12/01

Kemitron Electronics

PRM·2





COMPONENTS

C1	0·1μ Disc
2	100μ 10V
3	0·1μ Disc
4	100μ 10V
IC1-8	5204
9	74LS138
10	74LS139

NOTE ORIENTATION OF IC9, 10.

Kemitron Electronics

Drn. [illegible]	Date 14/12/78	PRM·2